

Search Notes

Application/Control No.

10/761,832

Examiner

Peter Coughlan

Applicant(s)/Patent under
Reexamination

ANDERSON ET AL.

Art Unit

2129

SEARCHED

Class	Subclass	Date	Examiner
700	1	7/6/2006	PDC
700	90	7/6/2006	PDC
706	1	7/6/2006	PDC
706	14	7/6/2006	PDC
706	45	7/6/2006	PDC
716	2	7/6/2006	PDC
455	458	7/6/2006	PDC
343	700	7/6/2006	PDC

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner

**SEARCH NOTES
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
East--circuit design, configuration, genetic algorithm, cost, time, power, parent, child, children, hewlett packard, minimum	7/6/2006	PDC
East--ll--function, repeat, iteration, optimize, threshold, limit, experssion, boolean, counter, flag, forecast, predict,	7/6/2006	PDC
IEEE--genetic algorithms, circuit design, configuration, predict, forcast, expression, boolean, counter, threshold,	7/6/2006	PDC
IEEE--predetermined limit, optimize, function, repeat, iteration, chromosomes, David Anderson, Tyson McGuffin, Thomas Chen	7/6/2006	PDC
Inventors David Anderson, Tyson McGuffin, Thomas Chen	7/6/2006	PDC
716.2 with genetic algorithm, adjusting, circuit, filter, evaluation function, changes values, threshold,	7/6/2006	PDC
716.2 with predetermined period, crossover, mutation, iteration, analyzer, timing	7/6/2006	PDC
455.458 with genetic algorithm, cost, parent, children, iteration,	7/6/2006	PDC